

VERSATILE ROUTING CALCULATION FOR DEADLOCK AVOIDANCE IN TORUS SYSTEMS UTILIZING VCT

*MOHAN. A, **Dr. P. SENTHILKUMAR,

*Associate Professor, Research Scholar (Anna University), Chennai – 602105, Tamil Nadu, India,

** Professor, SKR Engineering College, Chennai – 600123, Tamil Nadu, India,

*e-mail: annamalaimohan@gmail.com, ** e-mail: drpsenthilkumar2010@gmail.com

Abstract—Gridlock (Deadlock) has been an expansive issue in torus systems. Endeavors to battle this issue have been met with shifting outcomes. If there should be an occurrence of stop in the system, information is sent Using Routing Calculation i.e. Versatile Routing Calculation by means of VCT (Virtual-Cut Through) in torus systems. Beforehand the information is sent from the source to destination without checking the execution of the hubs, which prompts halt in specific circumstances. As an endeavor of beating the downside, before sending the information parcels, the execution of the hubs is measured. The execution measures that are taken in to thought are battery, memory and versatility. The way is found utilizing Greedy Routing Algorithm, if the hub is free from halt the information is sent else the trade-off hub is made use with the assistance of Virtual-Cut through (VCT).

Keywords--Virtual cut-through switching, wormhole switching, adaptive routing, deadlock-free, torus.

1. INTRODUCTION

Gridlock is a circumstance in which two PC programs having a similar asset are successfully keeping each other from getting to the asset, bringing about the two projects stopping to work. The most punctual PC working frameworks ran just a single program at any given moment. The majority of the assets of the framework were accessible to this one program. Afterward, working frameworks ran various projects without a moment's delay, interleaving them. Projects were required to indicate ahead of time what assets they required so they could maintain a strategic distance from clashes with different projects running in the meantime. In the end some working frameworks offered dynamic designation of assets. Projects could ask for facilitate distributions of packets after they had started running. This prompted the issue of the deadlock.

Lattices and tori are additionally extremely well known typologies for systems. Torus is a topology which has n-dimensional grid structure with k hubs in each measurement. In wormhole exchanging, each bundle is partitioned into a few stream control digits or dances. The main bounce of a parcel (called the header dance) contains the data for steering. The parcel is pipe-lined through the system at bounce level. At the point when a header bounce is obstructed amid transmission, all dances hold up at their neighborhood hubs. In VCT-exchanged systems, bundles can slice through to the following switch before the total parcel has been gotten. The principle

contrast between a VCT- exchanged and wormhole-exchanged system is that switches require support an entire parcel when blocking happens. A steering calculation decides the grouping of channels for packet to navigate from the source to the destination.

2. RELATED ISSUES

Introduces a new technique in irregular torus, a class of topologies proposed for metropolitan area networks, is more reliable than the bus and ring topologies [1]. There is a routing algorithm for the topologies which is space and time efficient so that it is suitable for high-speed implementations [1].

Buffer management will always have the control complexity at a node and the size of a network constant .The throughput of a typical irregular torus network is shown to be within a constant factor of that of a square torus [1]. It is fragile to topology perturbations due to network growth and node/link failure [1]. Deadlock avoidance techniques requiring a segregation of traffic in non-cyclic virtual networks and some form of injection control [2].

Currently, tori of five and six dimensions are being used in actual supercomputers [1,2]. Deadlock avoidance techniques for large high-dimension tori using traffic typical of parallel applications. The simulation tools had to be adapted to scale to simulate these large networks [2]. Simulating large high dimension tori network because synthetic traffic [2,3]. On-chip network which provides the same bisection bandwidth as a mesh network [3]. Deadlocks can be exactly detected without having to rely on a timeout mechanism and when needed, recover from the deadlock. As a result, DRT results in minimal loss in performance [3]. 2D torus can provide a more cost-delicate on-chip network. The on-chip network data path is reduced by $2\times$ while providing the different bisection bandwidth as a mesh network. Exploiting the abundant wires available while minimizing the need for additional buffers [3]. High-radix hierarchical networks are cost-effective topologies for large scale computers [4]. In such networks, routers are organized in super nodes, with local and global interconnections. These networks, known as Dragonflies [4].

Buffer memory requirements that constitute one of the main constraints in high-radix switch [4]. Global and local

misrouting are only allowed employing the same number of virtual channels. Depending on the traffic pattern, network congestion can lead to degraded performance [4].

Worm-Bubble Flow Control (WBFC), a new flow control scheme that can avoid deadlock in wormhole-switched tori using minimally 1-flit-sized buffers per VC and one VC in total[5]. Wormhole packets can span multiple routers, by creating additional channel dependencies and adding complexities in both deadlock and starvation avoidance [5,7]. Wormhole-switched topology with embedded rings can use WBFC to avoid deadlock but within each ring only. On-chip designs where area and power resources are greatly constrained are cost fragile [6,4]. This new method allows all virtual networks to share some common virtual channels [6]. Two virtual channels should be the lower bound for fully adaptive deadlock-free routing in tori because the dimension order routing for 2-dimensional tori also needs two virtual channels [6]. Based on routers that are performing online error *detection have difficulty in* path diversity of the network [6,4].

A tightly coupled multi-computer networks. A new routing algorithm is proposed that allows all nodes in the network to source and receive traffic [7]. The algorithm is efficient in the use of the network and should therefore be able to perform well even under heavy and busy traffic conditions [7]. Each process declares the maximum number of resources of each type which it may need [7]. The system's resource-allocation state is defined by the number of available and allocated resources, and the maximum possible demands of the processes but it is specified [7,8].

3. EXISTING SYSTEM

A gridlock (deadlock) free insignificant steering calculation called piece of information is first proposed for switched tori. Just two channels are required. One direct is connected in the gridlock free steering calculation for the work sub-arrange in view of a known base steering plan, for example, negative-first or measurement arrange directing. The other channel is like a versatile channel. This combinations displays a novel completely versatile negligible directing plan on the grounds that the main channel does not supply steering ways for each source-goal combine. Other two calculations named stream controlled piece of information and wormhole sign are proposed. Stream controlled intimation is proposed for exchanged tori, which is completely versatile negligible halt free with no virtual channel. Each information port requires no less than two supports, each of which can keep a bundle. A basic however all around composed stream control work is utilized as a part of the proposed stream controlled piece of information directing calculation to dodge gridlocks. Wormhole hint is proposed for wormhole-exchanged tori. It is incompletely versatile on the grounds that we add a few limitations to the versatile channels for gridlock evasion. It is demonstrated that piece of information and stream controlled intimation work superior to anything the air pocket stream control plot under a few prevalent activity designs in 3-dimensional (3D) torus. In wormhole-exchanged tori, the benefit of wormhole intimation over Duato's convention is

additionally exceptionally evident. The inconveniences of existing framework are:

- Data is send from source to destination without checking the hub execution.
- Range level is limited.

4. SYSTEM ARCHITECTURE

System architecture explains the entire architecture of the proposed system. Monitor the network helps to monitor entire nodes in the network. Using this monitoring of all the activities of the nodes are done. Node discovery is used to find the nodes available in the network. Performance measures such as battery, mobility and memory are being measured before sending the data through the nodes. Determining range and distance is used for the purpose of determining the range and distance between the neighboring nodes.

This Framework clarifies the whole design of the proposed systems. Screen the system it monitoring whole hubs in the system. Utilizing this observing of the considerable number of exercises of the hubs are finished. Hub revelation is utilized to discover the hubs accessible in the system. Execution measures, for example, battery, versatility and memory are being measured before sending the information through the hubs. Deciding extent and separation is utilized with the end goal of deciding the range and separation between the neighboring hubs.

Greedy Algorithm is used to find the path in which the data is sent from the source node to the destination node. The data is routed to the destined node. If there is no deadlock in the node the data is sent smoothly from the source node to destination node. In case of deadlock occurrence in the path, the data is not send. Through it in such a situation, the compromised node is being used. This makes use of compromised path. This compromised path is found using VCT (Virtual Cut-Through).

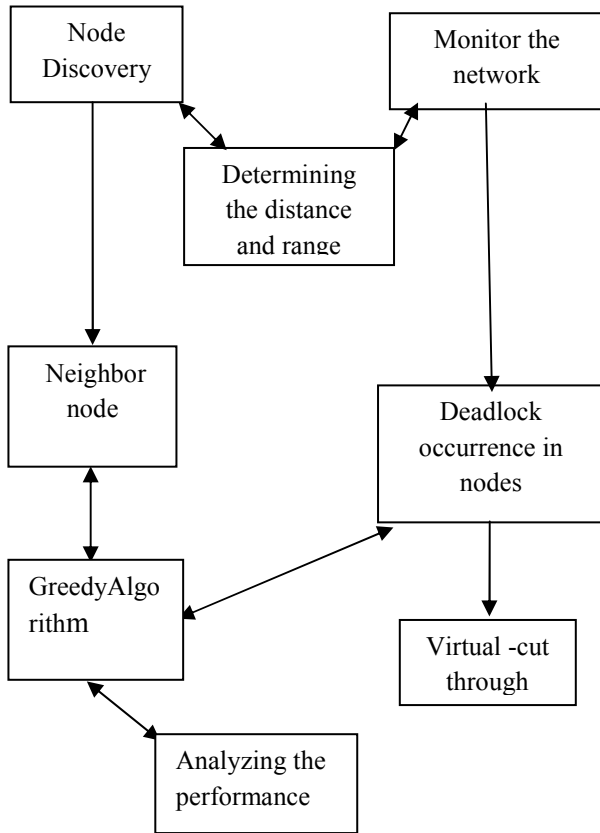


Figure 1. System Architecture

5. PROCESSING METHODOLOGY

The performance of the node is first checked and then the data packets are sent according to the availability of node. The performance measures measured are battery life, memory and mobility. The path of the node is found using greedy routing algorithm for the route discovery. When an attack occurs (i.e.) a deadlock, it is being reflected using greedy algorithm. The deadlock is eliminated by using VCT by which the data is sent through any other available neighboring node. Thus the data packets reach the destination by avoiding the deadlock. The advantages of proposed system are:

- Before sending the data, performance of the node is checked.
- No limitation of range between nodes.

5.1 Creation of Node

All links in an n_D torus are classified into mesh sub network links and wraparound links. In clue, two virtual channels (R1 and R2) are enough to provide deadlock-free fully adaptive minimal routing in a VCTswitched n_D torus.

Rule 1: A packet can request R1 channels at any time.

Rule 2: If a packet need not traverse any wraparound link from the current node to the destination, it can request R2 channels of the mesh sub network links. If a packet is routed on R2 channels, it must conform to deadlock-free minimal routing restrictions for meshes such as by negative-first or Dimension-order routing.

Rule 3: If the next hop of a packet can traverse a wraparound link of dimension d and d is the lowest of the

dimensions in which the packet needs to traverse, the packet can request the R2 channel of that wraparound link.

Start form consists of node design option. The node is created along with the performance measures such as mobility, battery, memory. This makes the node more reliable to send the data.

```

public void actionPerformed(ActionEvent e) {
    if(e.getActionCommand().equals("NodeDesign")){
        NodeDesign nodeDesign=new NodeDesign();
        nodeDesign.node_Design();
    }
}
  
```

NodeDesign() is a function that is used to design the node with various performance measures.

5.2 Routing Algorithm for Tori

The dimension-order routing algorithm is applied on R2 channels of the mesh sub network links. When a packet traverses from the source A to the destination H. The routing algorithm in a 2D torus is stated as follows: Because this algorithm is fully adaptive, packets can be routed along any minimal routing path. We just arbitrarily specify a routing path and states in this path which channel or channels can be requested.

A -> B, the packet need traverse two wraparound links from the source A to the destination H, where dimension x is the lowest dimension along which the packet need traverse a wraparound link. From Rule 1 and Rule 3, either R1 or R2 can be selected.

B -> C, the packet need traverse a wraparound link from B to H, while dimension y is the lowest dimension along which the packet need traverse a wraparound link. From Rule 1 and Rule 3 we know that, either R1 or R2 can be selected.

C -> E; F -> G; G -> H, the packet need traverse no wraparound link from the current node to the destination H. These hops follow the dimension order algorithm. As stated in Rule 2, R2 channels can be selected in these cases. R1 can also be selected. Therefore, either R1 or R2 can be selected

E -> F, the packet need traverse no wraparound link from E to H. This hop does not follow the dimension-order algorithm. From Rule 1 and Rule 2, R2 channel cannot be selected in this hop; only R1 channel can be selected.

Once the node is created its neighboring nodes are also created. The required destination node is placed in the destination node space and route is clicked. Once route is selected, using the Greedy based algorithm the path is being discovered. The routing algorithm in a torus is stated as follows:

```

public class RouteDiscovery {
    MulticastReceiver multicastReceiver;
    SocketConnection socketConnection=new SocketConnection();
    PathSendSerial pathSendSerial=null;
    NodeDesign nodeDesign;
    ReceiverLogic receiverLogic;
    public RouteDiscovery (MulticastReceiver multicastReceiver,
        NodeDesign nodeDesign, ReceiverLogic receiverLogic){
        this.multicastReceiver=multicastReceiver;
        this.nodeDesign=nodeDesign;
        this.receiverLogic=receiverLogic;
    }
}
  
```

Using RouteDiscovery the route to the destination is discovered.

ReceiverLogic is used to receive the sent data.

5.3 Deadlock Detection

To propose a new fully adaptive routing algorithm, named flow controlled clue, for n_D tori. The input buffers of flow controlled clue are organized as dynamically allocated multiqueues. Two queues instead are needed to avoid deadlocks. Its performance is even better. Two classes of packets, safe and unsafe packets, are defined in flow controlled clue. Based on a routing algorithm R for the mesh sub network of an n_D torus, a packet is safe to the downstream node in either one of the following conditions:

The next hop of the packet is to traverse a wraparound link along dimension d , and d is the lowest of the dimensions along which the packet need traverse wraparound links. The packet does not need to traverse any wraparound link from the current node to the destination.

The next hop is to reserve a link in the mesh sub network according to the routing function R.

While trying to send the information through the node, if deadlock occurs the data will not be sent forever. This condition is called as deadlock. While trying to send the data through a node which has deadlock, even before sending the data in the node, message regarding the deadlock is given. This indicates that the certain path has deadlock and compromise node should be taken.

```
elseif(objinstanceofAttackSendSerial){
AttackSendSerialattackSendSerial=(AttackSendSerial)obj;
attackName=attackSendSerial.attackNode;
System.out.println("+ .....receiver side..." +attackName);
}
```

AttackSendSerial is used to detect the node in which the deadlock has occurred. Then the attack is being reported at the receiver.

5.4 Deadlock Freedom Method

A packet is delivered in the mesh sub network when it need not traverse any wraparound link from the current node to the destination. Only at this time it could request R2 channels of the mesh sub network links. A deadlock-free routing algorithm for the mesh sub network is applied on R2 channels with the dimension order routing, the west-first routing, or the negative-first routing scheme. Therefore, the packet would not be blocked forever. The packet could always be delivered along R2 channels until reaching the destination.

Once the deadlock is detected in certain path the information sending shouldn't be stopped. The data should be sent to destined node from source. We use VCT (Virtual Cut-Through) to send the data from source node to destined node using a compromised node. This compromised path is being found using VCT. Now the data can be sent successfully to the destination node.

```
for(int i=0;i<neigh.length;i++){
String one_neighb=neigh[i].trim();
if(!one_neighb.equals(source)){
String port=(String)hm1.get(one_neighb);
String system=(String)hm2.get(one_neighb);
pathsendSerial=newPathSendSerial(source,destination,one_neighb,path,cost);
```

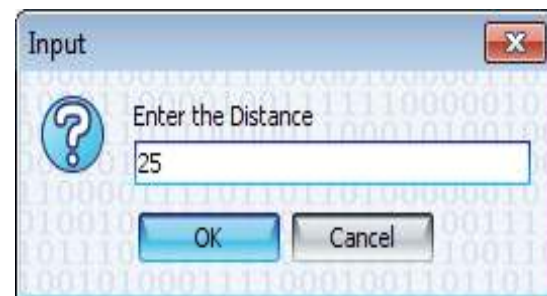
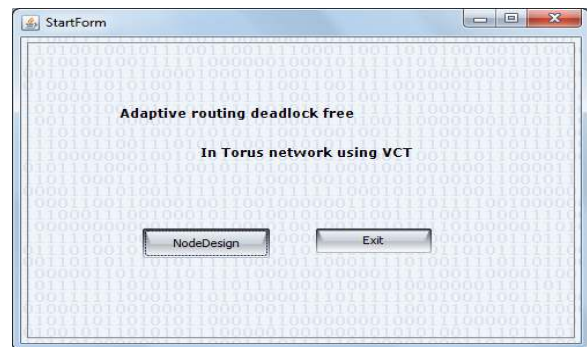
```
ObjectOutputStreamobjOutput
socketConnection.SocketSend(port,system);
objOutput.writeObject(pathsendSerial);
objOutput.close();
}
```

one_neighb is used to find the compromised path using VCT. PathSendSerial is used to send the data from source node to destination node using source, destination, one_neighb, path, and cost.

6. RESULTS AND DISCUSSION

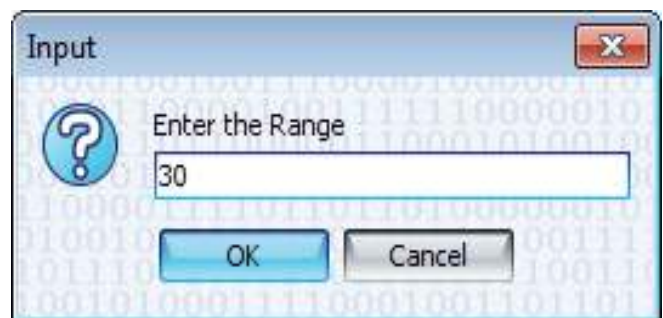
START FORM: DISTANCE

The basic start form for designing the required number of nodes. The distance between the node is entered

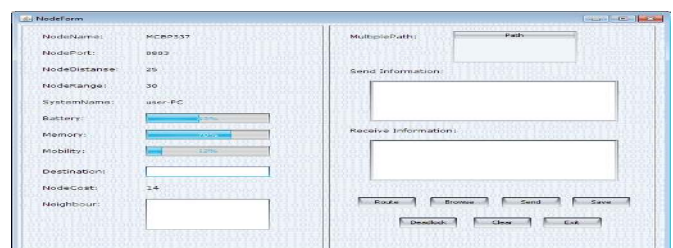


RANGE:

The range for each node is entered.

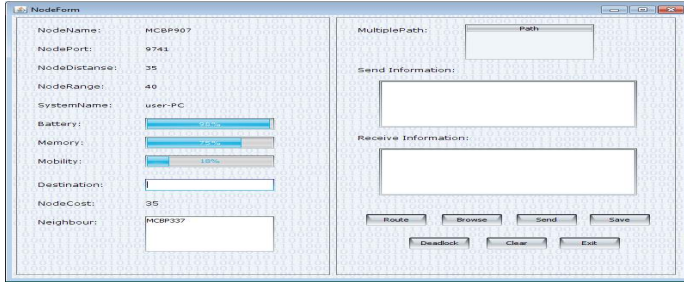


The complete action is performed with the help of node form.



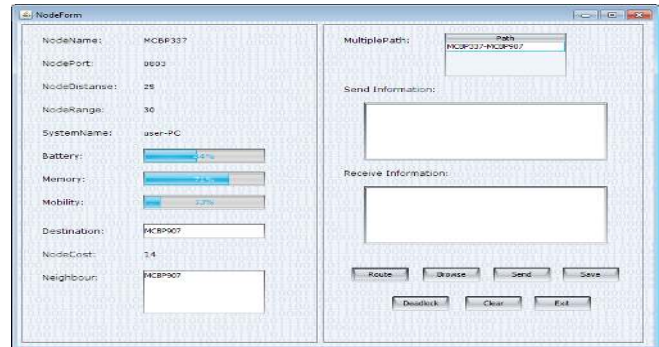
NEIGHBOUR NODE DESIGN

The neighbouring nodes are being created similar to that of the source node.



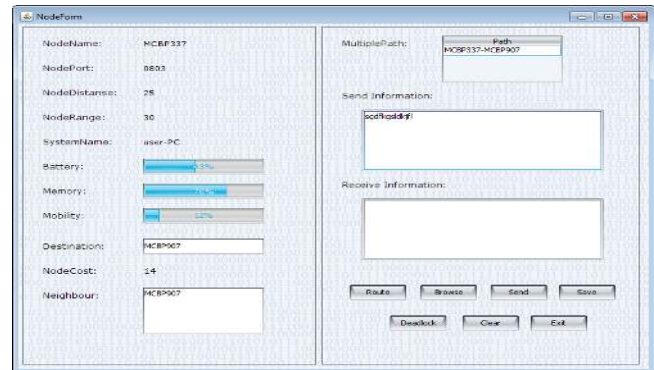
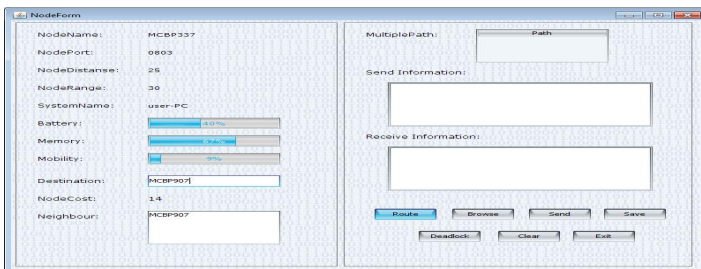
PATH SEND INFORMATION

The path to the destination is found. The information that has to be sent to destinations,



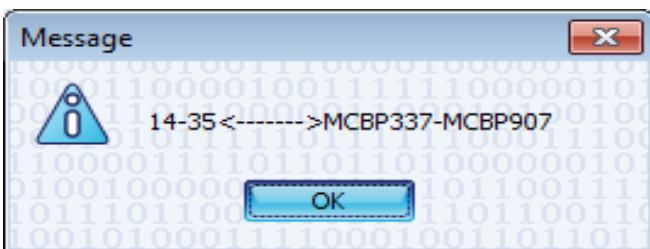
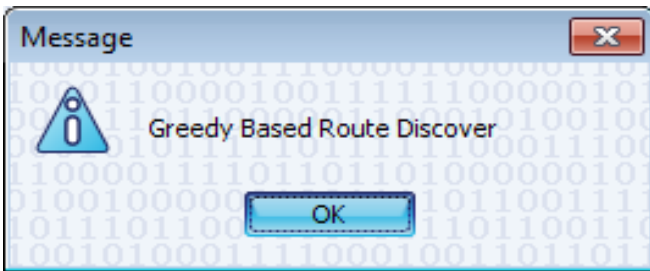
DESTINATION

In the text area provided for the destination the node name of the destination is being typed.



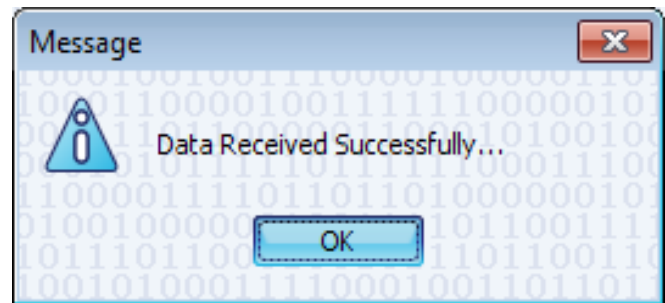
ROUTING:

The route is being discovered.



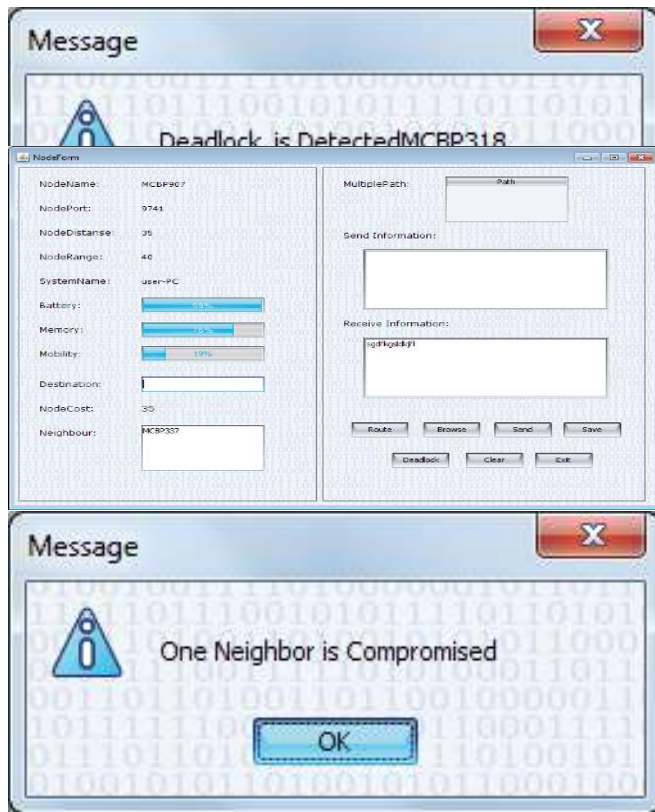
RECEIVE DATA:

The sent data is received successfully by the destination node



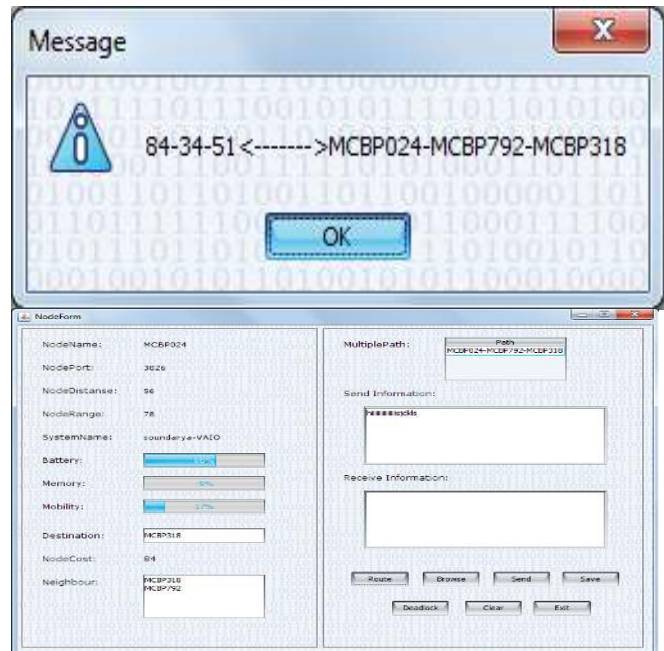
DEADLOCK MESSAGE COMPROMISED NODE

The deadlock message is prompted to the user. The Compromised path is choose using VCT



COMPROMISED PATH - SENDING DATA IN COMPROMISED PATH

The path of the compromised node is given here



7. CONCLUSIONS

Numerous halt free directing algorithms for introduced in this system (torus). For this situation just two compelling channels are compulsory for deadlock free versatile directing in torus organize. The way of the hub is discovered utilizing ravenous steering calculation for the course revelation. At the point when an attack happens (i.e.) a deadlock, it is being reflected utilizing greedy algorithm. The deadlock is disposed of by utilizing VCT by which the information is sent through some other accessible neighboring hub. Over the outcomes are contrasted and all neighbors hub, deadlock shirking in this system utilizing greedy algorithms.

8. FUTURE ENHANCEMENTS

- Following improvements can be executed in future
- ✓ Implementing a promptly accessible dynamic directing usage, for example, Zebra and Quagga. By breaking down the directing calculations on another steering daemon, advance approval can be accomplished.
- ✓ Analyzing other interconnect usage, for example, Ethernet or Fire wire.

REFERENCES

[1]Chen Y.M, Sasaki G. 'Irregular torus networks: deadlock avoidance and throughput analysis' IEEE Computer and Communications Vol.1, USA, 2013, pp.312- 321.

[2] Prisacari.B, Rodriguez.G, Minkenberg.C, Palacio R.B., 'Performance implications of deadlock avoidance techniques in torus networks' High Performance Switching and Routing (HPSR),IEEE-13th International Conference, Blegrade, Serbia, 2012, pp 55-58.

[3] Minjeong Shin, Kim J. 'Leveraging torus topology with deadlock recovery for cost-efficient on-chip network', Computer Design (ICCD), IEEE 29th International Conference, Boston Area, Massachusetts, USA, 2011, pp 31-33.

- [4] Garcia M. , Vallejo E. , Beivide R. , Odriozola M. , ‘Efficient Routing Mechanisms for Dragonfly Networks’ ,Parallel Processing (ICPP) , 42nd International Conference , Lyon, France ,2013, pp. 582 - 592.
- [5] Lihong Chen , Pinkston T.M. , ‘Worm-Bubble Flow Control’ High Performance Computer Architecture (HPCA) , IEEE 19th International Symposium , pp. 366 - 377, 2013
- [6] Dong Xiang , Yi Pan , Qi Wang , Chen Zhen, ‘Deadlock-Free Fully Adaptive Routing in 2- Dimensional Tori Based on New Virtual Network Partitioning Scheme’ Distributed Computing Systems(ICDCS) , The 28th International Conference , pp. 454 - 461, 2008.
- [7] Ngai J.Y. , Dhar S. ‘ A deadlock-free routing control algorithm for torus network based ATM switches’ , Discovering a New World of Communications , IEEE International Conference , Chicago, USA, 1992, pp. 709 - 713.
- [8] D. Xiang, “Deadlock-free Adaptive routing in Meshes with Fault-Tolerance Ability Based on Channel Overlapping,” IEEE Trans. Dependable and Secure Computing, vol. 8, no. 1, pp. 74-88, Jan./Feb. 2011.
- [9] M. Ould-Khaoua, “A Performance Model for Duato’s Fully Adaptive Routing Algorithm in k- Ary n-Cubes,” IEEE Trans. Computers, vol.48, no. 12, pp. 1297-1304, Dec. 1999.
- [10] W. Dally and B. Towles, Principles and Practices of Interconnection Networks. Morgan Kaufmann Publishers, Inc., 2016.